

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	107	(wiring adj pattern) and pad and terminal and (slit or aperture or hole) and width.clm.	US-PGPUB
2	BRS	L2	0	(wiring and pattern and pad and terminal and slit and width).clm.	US-PGPUB
3	BRS	L3	3	(wiring and pattern and pad and slit and width).clm.	US-PGPUB
4	BRS	L4	0	((wiring adj pattern) and pad and terminal and (slit or aperture or hole) and width).clm.	US-PGPUB
5	BRS	L5	601	257/775.ccls.	US-PGPUB; USPAT; USOCR
6	BRS	L7	1910	257/773.ccls.	US-PGPUB; USPAT; USOCR
7	BRS	L8	1516	257/786.ccls.	US-PGPUB; USPAT; USOCR
8	BRS	L9	1730	257/737.ccls.	US-PGPUB; USPAT; USOCR
9	BRS	L10	1641	257/738.ccls.	US-PGPUB; USPAT; USOCR
10	BRS	L11	1211	257/780.ccls.	US-PGPUB; USPAT; USOCR
11	BRS	L12	3185	Chip adj scale adj package	US-PGPUB; USPAT; USOCR
12	BRS	L13	121	257/775.ccls.	EPO; JPO; DERWENT; IBM_TDB
13	BRS	L14	449	257/773.ccls.	EPO; JPO; DERWENT; IBM_TDB
14	BRS	L15	356	257/786.ccls.	EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
15	BRS	L16	235	257/737.ccls.	EPO; JPO; DERWENT; IBM_TDB
16	BRS	L17	1111	257/690.ccls.	US-PGPUB; USPAT; USOCR